

S-161B  
2-204  
bw

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Ely

Application No.: 09/679,461

Filed: 10/04/2000

Title: System, Device and Method for Controlling  
Access to Memory12024 BA US014  
Attorney Docket No.: 2494/103 120-034

Group Art Unit: 2186

RECEIVED  
CENTRAL FAX CENTER

Examiner: Li

JAN 26 2004

OFFICIAL

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450RESPONSE UNDER 37 CFR 1.111

Dear Sir:

In response to the Office Action of August 26, 2003, please amend the claims to this application as shown on the attached sheets.

REMARKS

Reconsideration and further examination is respectfully requested.

Claim 32 has been rejected under 35 U.S.C. §102(b) as being anticipated by Bauman et al (U.S. Patent 5,875,472).

Bauman describes an improved conflict detection system for use in maintaining memory coherency in a multiprocessor, shared cache memory. In particular, Bauman describes, at column 8, lines 42-56, in part:

"... During execution, an IP will generally attempt to retrieve data signals (either representing instructions or operands) from its associated first-level cache. If the requested data